Internship/Thesis - AI supported hardware development and maintenance process

Description

In Televic Rail we make solutions for passenger communication systems. These components exist of LED displays, TFT displays, train controllers and audio devices. All these systems are made from Televic developed HW platforms (based on Printed circuit Board Assemblies - PBAs). These PBAs are designed and validated. The design is done through the Altium365 tool suite, in which the schematic entry and PCB layout is performed.

In this thesis we want to investigate and implement automation with AI support in hardware development & maintenance process. The AI support could be applied in different aspects:

- Altium365 design
- Document generation (based on specs, board schematic, validation, …)
- Reliability or MTTF calculation
- Checks related to compliancy (RoHs, Reach, ...)
- Prediction of possible obsolescence
- Detection of trends, issues from different sources of production data
- ....

About Televic Rail

With over 30 years of experience in designing, manufacturing and maintaining on-board communication and control systems, Televic Rail is a leading, trusted partner for railway operators and train builders worldwide.

Its Passenger Information Systems and Control Systems are high quality, tailor-made solutions that offer the flexibility, user-friendliness and stability that our clients ask for. Our various types of on-board control systems such as our bogie monitoring systems are innovative, yet reliable products designed specifically for the railway business.

Trains and trams all around the world are equipped with Televic Rail solutions, from New Zealand to Canada, from China to the United States, from India to Belgium, England and France.

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Nature of the work				
Level	**Keywords**	**Type of work**	**Location**	**Num. of students**
Bachelor, Master	AI, Machine Learning, Electronics, Hardware	Research: 20% Implementation: 50% Experimentation: 30%	Televic	1